

Receipt
FILE COPY

Docket No.: 49657-700

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tsutomu YOSHIMURA, et al.

Serial No.: 09/584,728

Filed: June 01, 2000

Group Art Unit: 2816

Examiner:

For: **DIGITAL SYNCHRONOUS CIRCUIT FOR STABLY GENERATING OUTPUT
CLOCK SYNCHRONIZED WITH INPUT DATA**



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REQUEST FOR CORRECTED FILING RECEIPT


Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the name of the second inventor is misspelled. Attached is a copy of the executed Declaration which contains the correct spelling of the name of the second inventor. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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FILING RECEIPT



OC00000005272163

UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark OfficeAddress: ASSISTANT SECRETARY AND
COMMISSIONER OF PATENT AND TRADEMARKS
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/584,728	06/01/2000	2816	690	49657-700	6	13	1

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AUG 30 2000

McDermott, Will & Emery

Date Mailed: 07/26/2000

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Applicant(s)

Tutomu Yoshimura, Hyogo, JAPAN;
~~Harafusa~~ Kondoh, Hyogo, JAPAN;

HARUFUSA

Continuing Data as Claimed by Applicant

Foreign Applications

JAPAN 11-347448(P) 12/07/1999

If Required, Foreign Filing License Granted 07/26/2000

Title

Digital synchronous circuit for stably generating output clock synchronized with input data

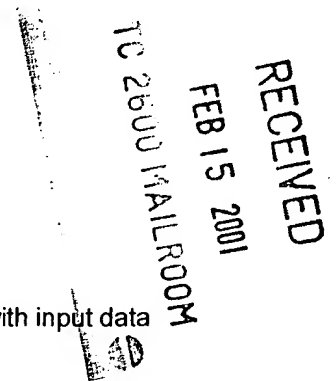
Preliminary Class

327

Data entry by : SHIFERAW, MESERET

Team : OIPE

Date: 07/26/2000



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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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